Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	363139	(device peripheral) adj (active used)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:55
L2	1145	L1 near7 (SMI interrupt event)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:55
L3	59	(L1 near7 (SMI interrupt event)). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:55
L4	4950	713/300,322,323,324.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:56
L5	2	L3 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:56
L6	72882	(CPU process\$4) adj speed	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:56
L7	25944	L6 near3 (transition\$4 increas\$4 decreas\$4 low\$3 high\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:56
L8	747	L7 near3 (control\$4 manag\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:56

L9	. 15	L8 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:56
L10	4950	713/300,322,323,324.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:56
L11	139182	(CPU processor microprocessor process\$4) near7 (SMI interrupt event)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:56
L12	47	L2 near3 L11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:58
L13	2	L10 and L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:58
L14	0	(L10 and L12).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:58
L15	11	(L2 near3 L11).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 18:58
S1	5161	(CPU processor) adj speed	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/28 17:00

S2	926	S1 near3 (transition\$4 increas\$4 decreas\$4 low\$3 high\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:21
S3	22	S2 near3 (control\$4 manag\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:21
S4	51610	(CPU processor) near3 (frequency speed)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/08 07:31
S5	19051	S4 near3 (transition\$4 increas\$4 decreas\$4 low\$3 high\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:47
S6	1936	S5 near3 (control\$4 manag\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/08 07:39
S7	780	(bus adj master adj device)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	OFF	2006/09/08 07:33
S8	16689	(bus adj master adj device) (network adj (card adapter))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/08 07:33
S9	1407	S8 near3 (active us\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/08 07:34

S10	28	S9 near3 determin\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/08 07:36
S11	2	S10 same S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/08 07:37
S12	2	S10 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/08 07:37
S13	76	S5 near3 (cancel\$4 stop\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/08 07:39
S14	64090	(CPU process\$4) adj speed	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:20
S15	22951	S14 near3 (transition\$4 increas\$4 decreas\$4 low\$3 high\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:21
S16	684	S15 near3 (control\$4 manag\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:22
S17	16693	(bus adj master adj device) (network adj (card adapter))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:22

S18	2	S16 same S17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:22
S19	3863	713/300,322,323,324.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:24
S20	11	S16 and S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/11 12:24
S21	8155	(system adj management adj interrupt) SMI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/07 11:14
S22	75	(second 2nd another next) adj S21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR.	OFF	2007/02/07 11:14
S23	6	S22 with device	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/07 11:15
S24	7	S22 with (device keyboard mouse display printer scanner)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/07 11:15
S25	54409	(CPU processor) near3 (frequency speed)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:29

S26	20067	S25 near3 (transition\$4 increas\$4 decreas\$4 low\$3 high\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:29
S27	2058	S26 near3 (control\$4 manag\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:36
S28	1604	determin\$4 near3 (device peripheral) near3 (active sleep idle)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:48
S29	1	S27 near7 S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:32
S30	1	S27 same S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:33
S31	3	S27 and S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:35
S32	377	((system adj management adj interrupt) SMI) adj signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:35
S33	1523	S26 near3 (perform\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:37

S34	1	S32 near7 S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:37
S35	0	S28 near7 S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	OFF	2007/02/21 07:37
S36	1	S28 same S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/21 07:37
S37	54453	(CPU processor) near3 (frequency speed)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:47
S38	20085	S37 near3 (transition\$4 increas\$4 decreas\$4 low\$3 high\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:47
S39	1605	(determin\$4 determin\$4) near3 (device peripheral) near3 (active sleep idle)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:50
S40	2	S38 with S39	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:50
S41	3484	(determin\$4 detect\$4) near3 (device peripheral) near3 (active sleep idle)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:49

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S42	2	S38 with S41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	OR	OFF	2007/02/22 06:56
S43	102878	(device peripheral) near3 (active sleep idle)	IBM_TDB US-PGPUB; USPAT; USOCR;	OR	OFF	2007/02/22 06:50
			EPO; JPO; DERWENT; IBM_TDB	·		
S44	11	S38 with S43	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:51
S45	2	S44 same (interrupt (system adj management adj interrupt) SMI event)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:55
S46	54282	(receiv\$4 get\$4 alert\$4) near3 (interrupt (system adj management adj interrupt) SMI event)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:55
S47	1	S46 near3 S41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:56
S48	1	S46 near7 S41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:56
S49	14	S46 same S41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:57

S50	11	S46 with S41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/22 06:57
S51	1899	((reduc\$4 adj power) ((speed state) adj transition\$4)) near7 (SMI interrupt event)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:24
S52	437	S51 near7 (processor cpu microprocessor process\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:24
S53	49	S52 near7 (device peripheral controller manager)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:25
S54	4276	713/300,322,323,324.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:25
S55	6	S54 and S53	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:40
S56	335995	(device peripheral) adj (active used)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:42
S57	1059	S56 near7 (SMI interrupt event)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:43

S58	127164	(CPU processor microprocessor process\$4) near7 (SMI interrupt event)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:44
S59	45	S57 near3 S58	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:56
S60	16220	S54 abd S59	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:44
S61	2	S54 and S59	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:45
S62	518	(reduc\$4 adj (speed frequency)) near3 (CPU processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:57
S63	910872	(device peripheral) near3 (active sleep idle used)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:58
S64	0	S62 near7 S63	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:58
S65	0	S62 with S63	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 08:58

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S66	8	S62 same S63	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 09:05
S67	132	S62 and S63	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	·OFF	2007/02/27 08:58
S69	6386	(transition\$4 adjust\$4 decreas\$4 increas\$4 lower\$4) near3 (processor cpu) near3 (speed frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 09:07
S70	9	S63 near7 S69	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/27 09:07
S71	29	(interrupt near2 signal) near3 ((based adj on) result\$4) near3 timer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/28 17:01
S72	4656	713/300,322,323,324.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/28 17:01
S73	0	S71 and S72	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2007/07/28 17:01